Analog Layout Design

Learn schematic-to-layout methods for analog blocks, with emphasis on matching, parasitics, isolation and physical verification.

What you will study

01	**Layout foundations** Analog layout flow; device layers; design rules; CMOS and passive device layouts.
02	**Precision techniques** Common-centroid and interdigitated placement; matching; parasitics and layout-dependent effects.
03	**Planning and isolation** Floorplanning; area and power routing; guard rings; shielding; substrate isolation.
04	**Verification** DRC and LVS checks, error interpretation and correction.
05	**Advanced blocks** High-speed layout considerations; op-amp, bandgap and converter sub-block layouts; ESD and IO cell planning.
06	**Practice and review** Analog block layout exercise, physical verification and final review.

Batch and learning format

For confirmed start dates, visit the live batch calendar. Online and Hyderabad/Bengaluru classroom options are advertised. Ask admissions to confirm the timetable, seat availability, fees and current lab access for your chosen batch.

Explore the current course page

https://chipxpert.in/analog-layout-design-2/

For admissions: +91 83098 18310 | chipxpert.in/contact/